

- **SMALL memory model**
.MODEL SMALL

The small [memory](#) model is limited to a single combined segment of 64k bytes. This segment is a combination of the stack, code and data segments. The assembler directive used to specify a small memory model is,

- **LARGE memory model**

.MODEL LARGE

The [large memory](#) model supports multiple segments, each segment limited to 64k bytes. The code and stack segments are limited to 64k bytes each, but we can have two data segments of 64k bytes each. The assembler directive used to specify a large memory model is,

What's the difference between “CLD” and “STD” instructions in assembly language?

CLD: clear D bit in the flag register (D=0) this means that we will make data growth upward.

STD: Set D bit in the flag register (D=1) this means that we will make the data growth downward.